**EE 205 Circuit Theory**

**Lab 9**

**Steady State Sinusoidal Response (Phasor Analysis)**

**Procedure:**

Consider the RC circuit shown in Fig.1 with C=1uF, R=10. Use a cos source signal with 10V peak and 0V offset at 100kHz frequency. We aim to find the steady state VC(t).

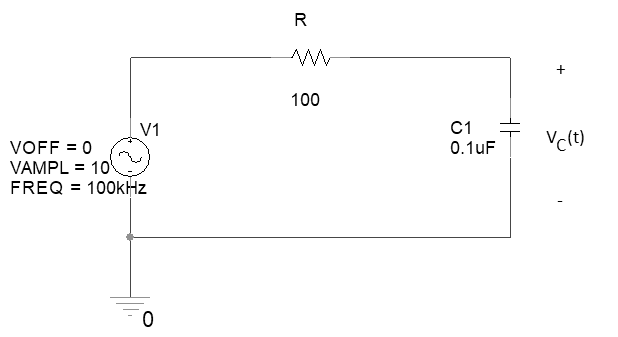


Fig.1. Steady State RC Circuit

The phasor impedance of the capacitor is

Thus, the capacitor voltage phasor is

The time expression of the capacitor voltage is

Table 1. Calculated and Measured Values

|  |  |  |  |
| --- | --- | --- | --- |
| Calculated Values for VC | | Measured Values for VC | |
| (V) | (degrees) | (V) | (degrees) |
|  |  |  |  |